



DUMA VIDEO
Low Latency MPEG Codecs

Akali

Xilinx MPEG-2 Decoder FPGA IP

The Akali MPEG2 Decoder IP is a standards compliant FPGA core. The FPGA design is a highly flexible core which allows for seamless integration into complex designs. The Decoder is also optimized for low-delay operation and, when paired with Duma's MPEG-2 Encoder, it is the IP of choice for RF video links, industrial control, video conferencing, and surveillance.

The Duma Video team is dedicated solely to video compression and decompression. We were the first to have a working real-time HD solution on FPGA.

Design services are available upon request.

FEATURES

- Chroma 4:2:0 and 4:2:2
- Video resolution up to 1080p/30fps
- NTSC and PAL
- Video and transport bit rates up to 100 Mb/s
- Accepts DVB ASI deserialized stream
- MPEG layer 1 and 2 audio capable
- Low delay
- Standards compliant
- Video scalar
- Low power
- All code written by Duma Video
- Flexible licensing terms

SPECIFICATIONS

FPGAs supported	Virtex-6, Virtex-7
License options	One-time fee or per unit royalty
Control interfaces	I2C, PCI, as well as proprietary interfaces
Audio/Video Interfaces	I2S, SMPTE 125m/274m, BT 656/601, DVI/HDMI and others
Physical size	1 chip
Operating temp	0 to 85 C

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